

# THE SiC/SiO<sub>2</sub> INTERFACE STRUCTURE

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**D A R P A / E P R I M E G A W A T T R E V I E W**

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**V a n d e r b i l t U n i v e r s i t y / A u b u r n U n i v e r s i t y / O a k R i d g e**

**O v e r v i e w**

- 1 ) The focus of our effort has been to improve the characteristics (defect density) of the SiC/SiO<sub>2</sub> interface, which currently limits the useful range of 4H-SiC Power MOSFETs.
- 2 ) At the previous meeting in this series we reported the significant finding that a substantial decrease in defect density, D<sub>it</sub>, of this interface results from annealing in nitrogen ambients. These effects were explained in terms of nitrogen induced passivation of carbon (cluster) defects.
- 3 ) In this session we report results which build on this nitrogen process. In particular, we show:
  - i) Quantitative measures of N incorporation as a function of time and temperature;
  - ii) New D<sub>it</sub> data which explores the N effect as a function of defect position in the band-gap;
  - iii) MOSFET mobility data, showing significant mobility improvement due to N;
  - iv) Theory, explaining the N band-gap dependence and C-O removal.

# Nitrogen Incorporation

## 1. NO process-

G.Y. Chung et al. APL, 76, 1713(2000)

D (units)= 1.0E11/cm<sup>2</sup> eV

$$4H-D_{it}(E_c)=25, \mu_{4H}=35-70$$

$\mu$  (units)=cm<sup>2</sup>/Vsec

## 2. NH<sub>3</sub> process-

G.Y. Chung et al APL (to be published)

$$4H-D_{it}(E_c)=20, \mu_{4H}=?$$

## 3. N<sub>2</sub>O-process-

Dimitrijev et al. EDL, 18, 175 (1997)

$$6H-D_{it}(O_2)=1.1, D_{it}(O_2+NO)=0.3, D_{it}(O_2+N_2O)=1.3; \mu=?$$

## 4. N ion implantation-not reported (?)

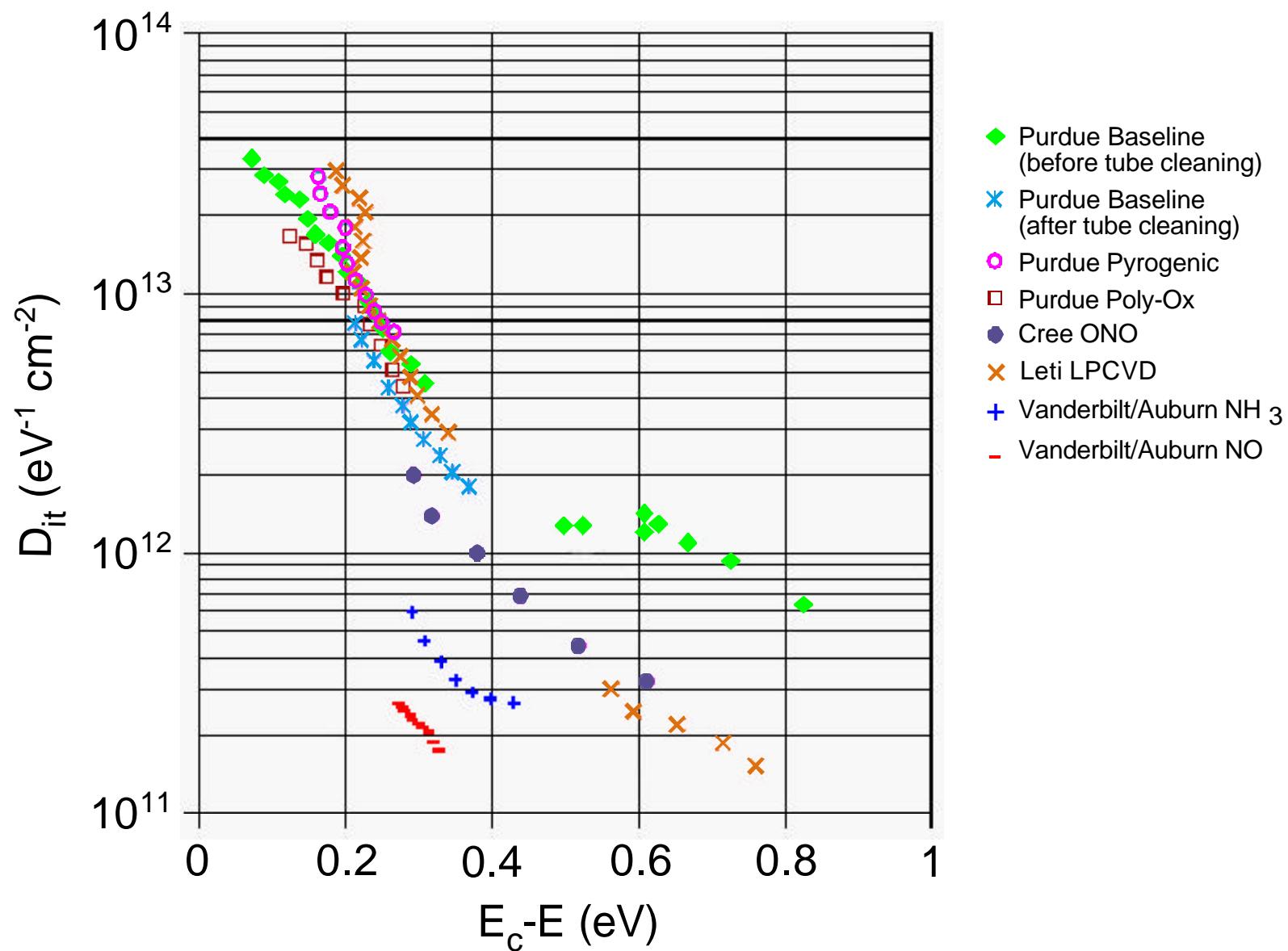
Sridevan and Baliga, Matls.Sci Forum, **264**, 997 (1998)

$$\mu_{4H}=160; \mu_{6H}=110; N_{eff}=5.0$$

## 5. Deposited oxynitrides-

Ma et al (JVD); Lipkin and Palmour (ISCRM)

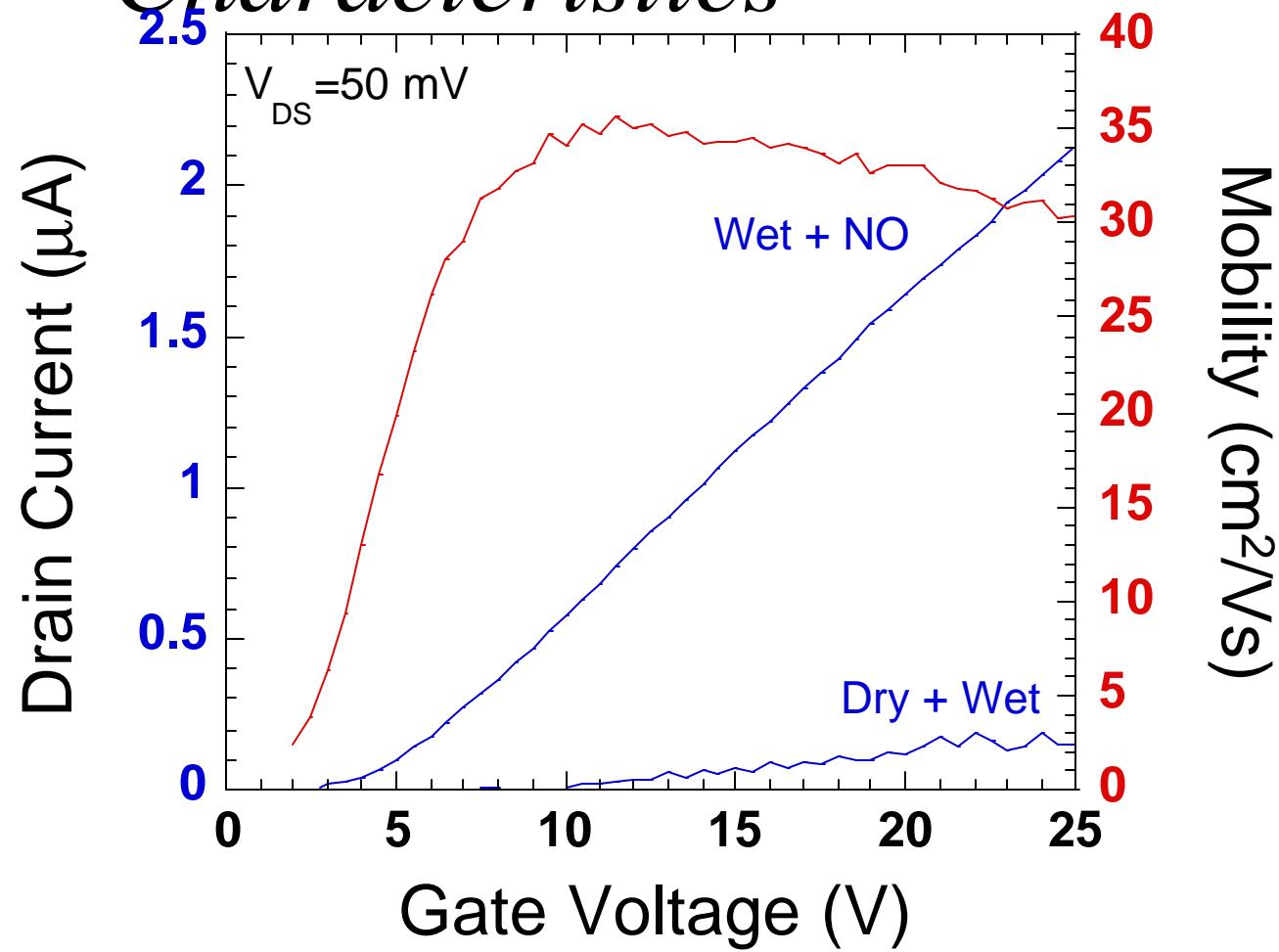
$$\mu_{4H}(ONO)=40, \mu_{4H}(O)=35$$



# *4H-SiC MOSFET*

## *Transfer*

### *Characteristics*



## **OUTSTANDING 4H-MOBILITY RESULTS**

1.  $165 \text{ cm}^2/\text{Vsec}$ ; Sridevan and Baliga, EDL 19,228 (1998)

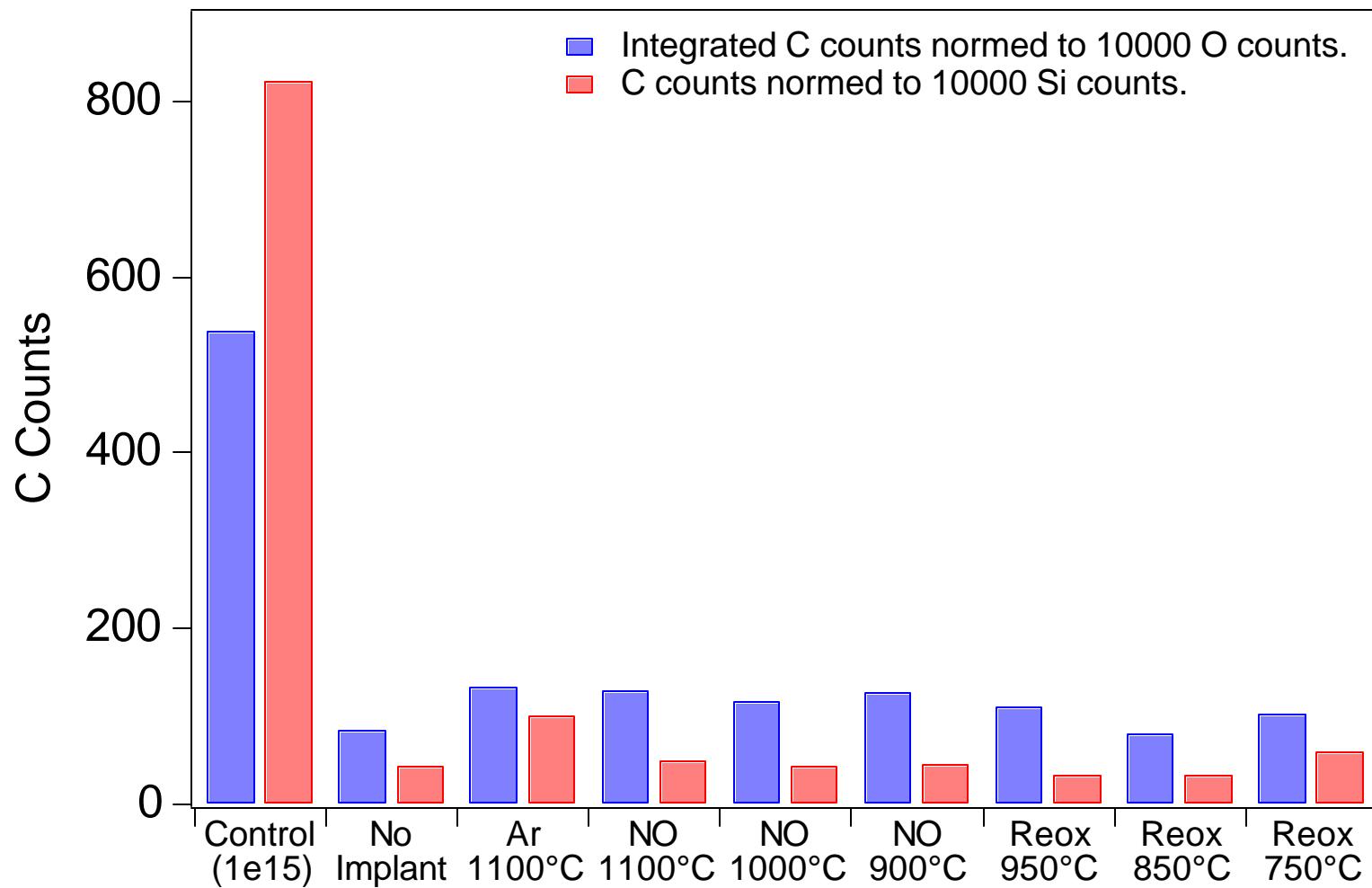
Possibly a buried channel device due to nitrogen implantation.  $R_p$  (40Kev N/SiO<sub>2</sub>)=800A+-300A with a 1000A oxide.

2.  $96\text{cm}^2/\text{Vsec}$ ; Yano et al, Jap,J. Appl. Phys. 39, (2000)

(1120) face, non-polar surface.

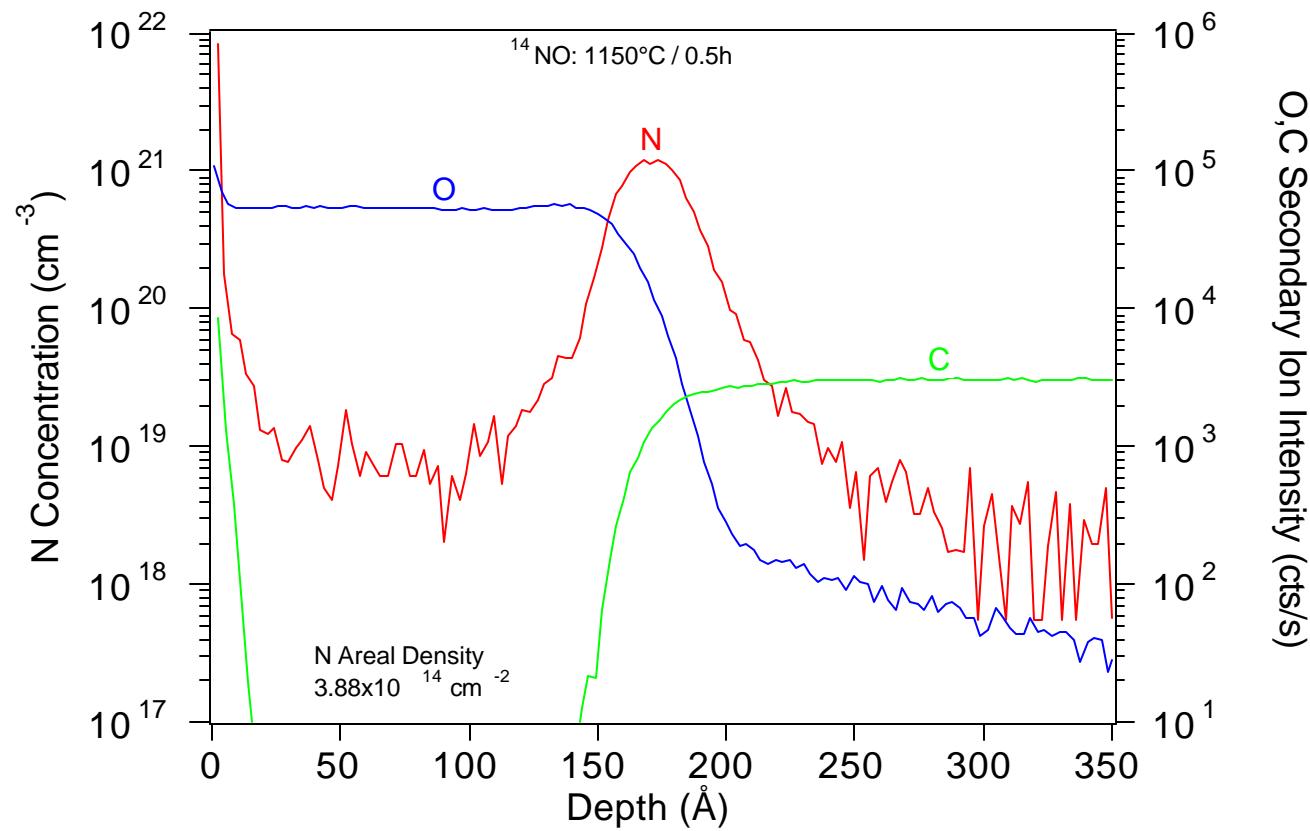
3.  $35\text{-}70\text{cm}^2/\text{Vsec}$ ; Das, Chung, McDonald et al. APL, EDL (2000)

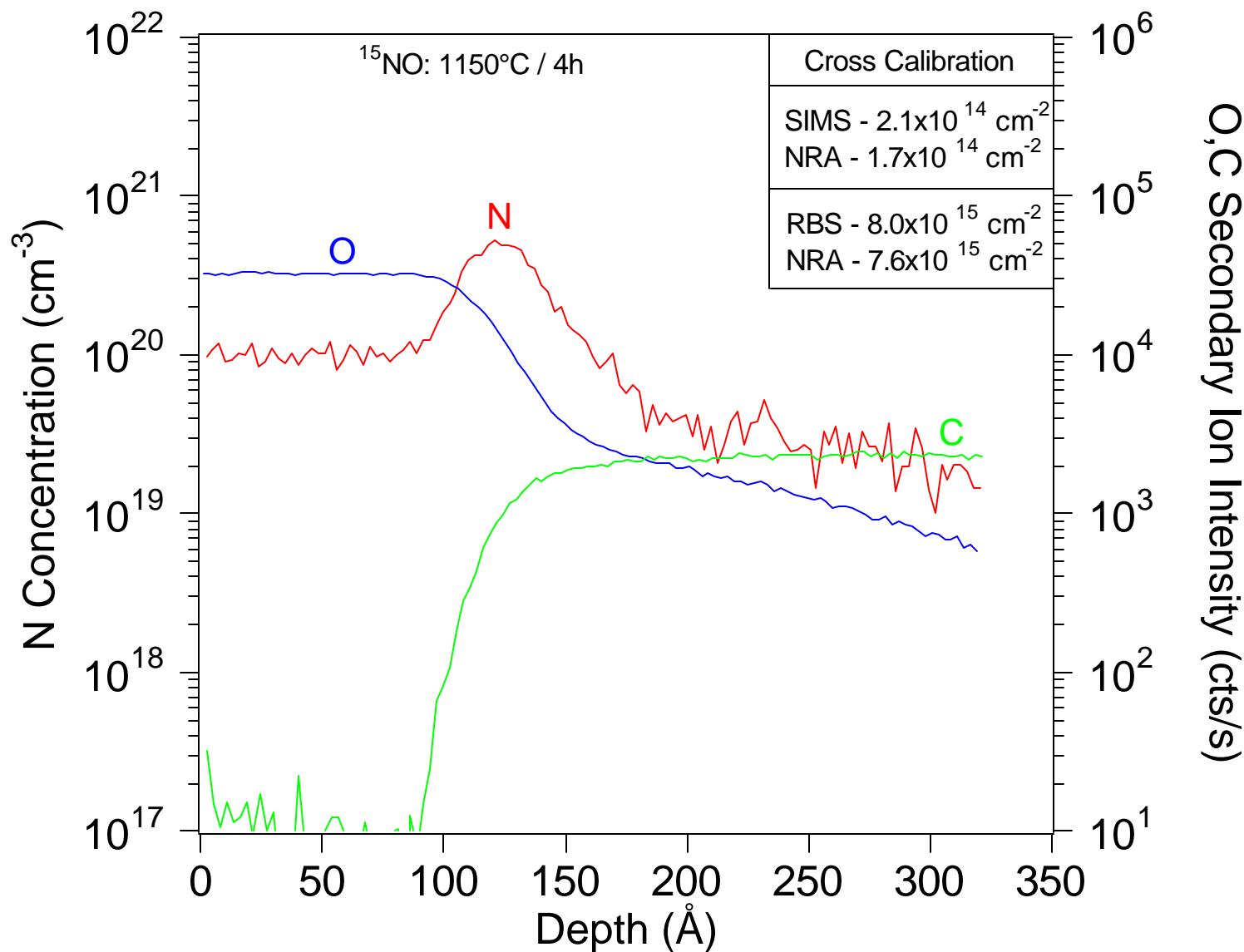
NO annealing after oxidation.

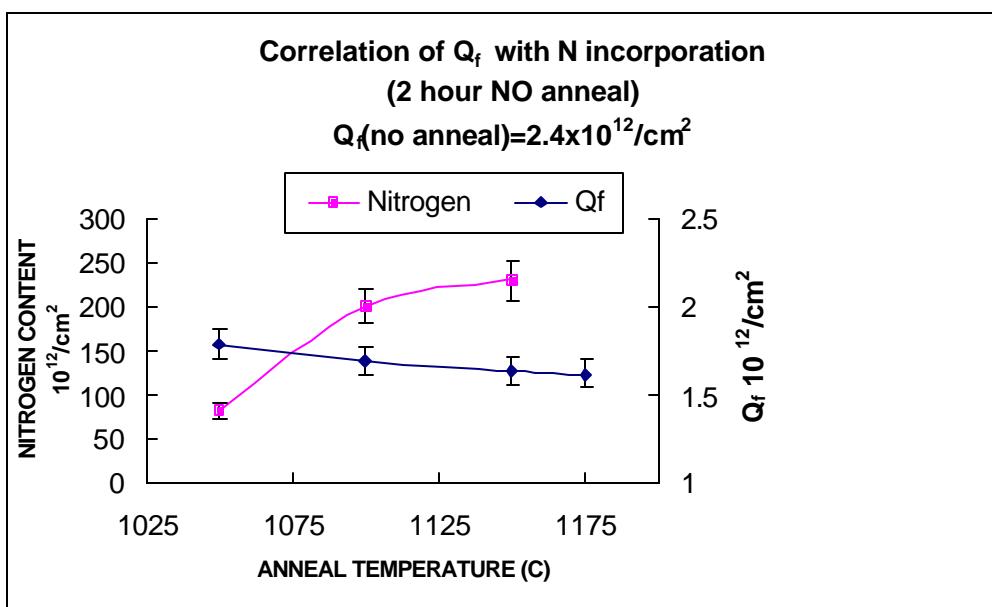


# Secondary Ion Mass Spectrometry

Interface nitrogen incorporation  
from NO annealing process

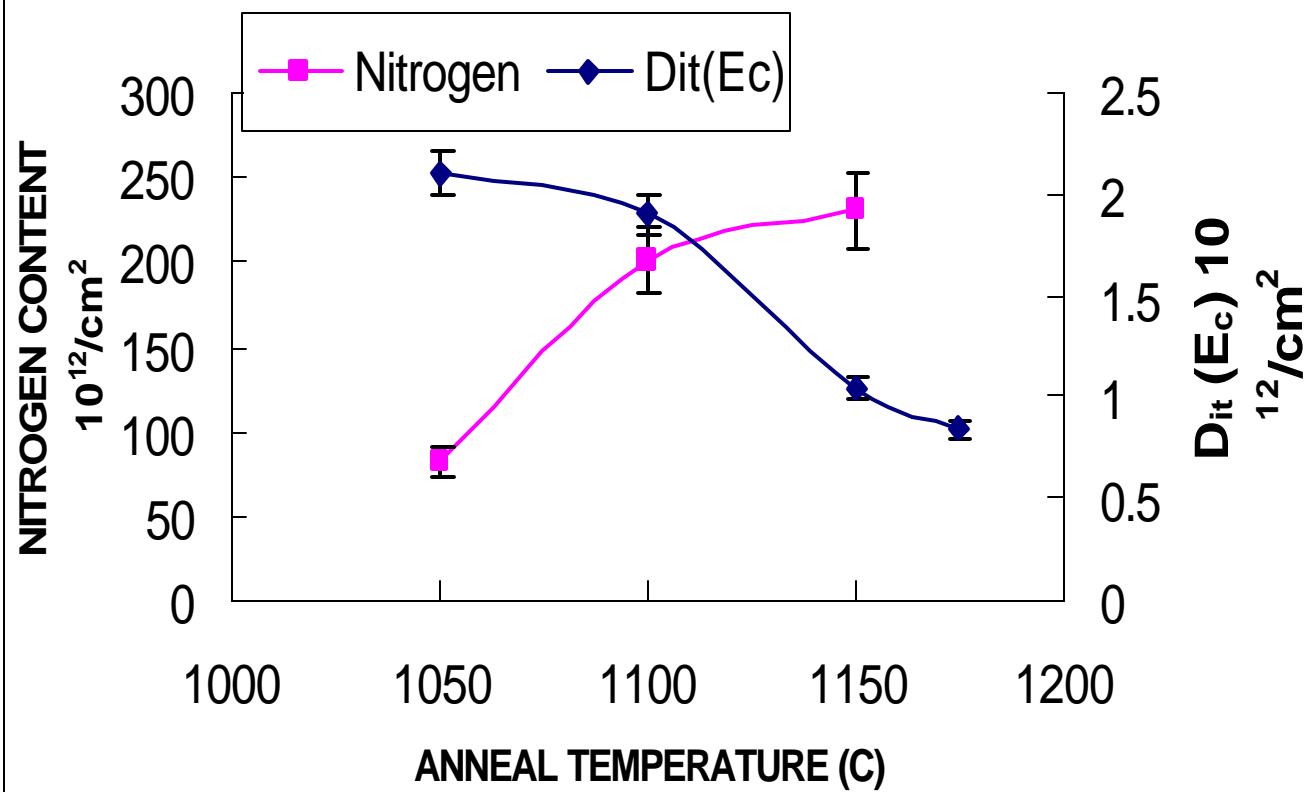






## Correlation of $D_{it}$ Decrease with N incorporation (2 hour NO anneal)

$$D_{it}(\text{no anneal}) = 6.8 \times 10^{12}/\text{cm}^2$$



## Nitrogen at the Interface?

1. **Stress relaxation**- thermal expansion difference and “atom density” cause stress. Even though the nitride-silicon difference is of opposite sign to the oxide-silicon difference there is not enough nitrogen to make this plausible.
2. **Interface chemistry**-molecular orbital calculations for silicon indicate that interface chemistry is the predominant driving force for interfacial silicon. (Ushio et al., APL 75, 680, (1999). Note that Ushio also calculate stress (for Si/SiO<sub>2</sub>) and show it does not work. Si/SiO<sub>2</sub> stress is comparable to SiC/SiO<sub>2</sub>.
3. **Voids**-positrons see voids at the Si/SiO<sub>2</sub> interface. Possibly the nitrogen species is simply filling voids.

